

FEATURES:

- Magnetic designs to support every PHY
- Meet or exceed IEEE 802.3af and ANSI X3.263 standards
- Meet IEEE 802.3at standards
- including 350uH min OCL with 8mA bias
- Minimum 2250VDC isolation per IEEE 802.3 requirement
- Designed for 100 base transmission over UTP-5 cable
- Size same as RJ-45 modular jack to save PCB board Space
- Recognized by UL 60950-1

YUAN DEAN SCIENTIFIC



**RJ45 Jack
13F-67 SERIES**

With Magnetics Modules

Single Port

10/100 Base-TX

PoE & PoE+

TAB Down



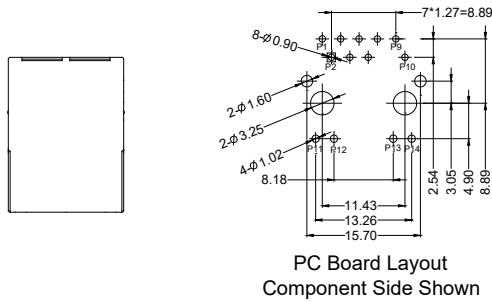
Electrical Specifications@25°C-Operating Temperature 0°C to 70°C

Part Number	Turns Ratio Pri:Sec. ±3%		EMI Fingers	LED (L/R)	Insertion Loss (dB max)	Return Loss (dB min @100 Ω)					Crosstalk (dB TYP.)	Common Mode Rejection (dB TYP.)			POE Current	Hipot (VDC)	
	TX	RX				0.3-100 MHz	0.5-30 MHz	40 MHz	50 MHz	60-80 MHz		0.3-100 MHz	0.3-30 MHz	30-60 MHz			60-100 MHz
13F-671BYGDNW2NL	1CT:1CT	1CT:1CT	YES	Y/G	-1.1	-18	-15.5	-13.6	-12	-38	-50	-40	-30	700mA	2250		
13F-67CGYDS2NL	1CT:1CT	1CT:1CT	YES	G/Y	-1.1	-18	-15.5	-13.6	-12	-38	-50	-40	-30	350mA	2250		



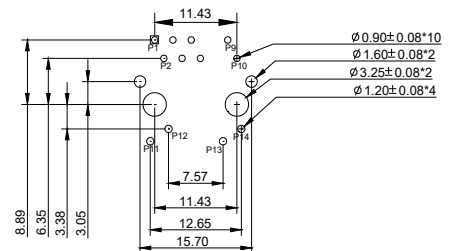
Mechanical and Dimension

13F-671B



PC Board Layout
Component Side Shown

13F-67C

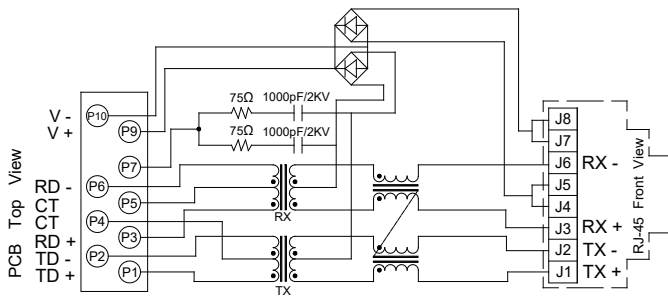


PC Board Layout
Component Side Shown

Unit: mm Unless otherwise specified, all tolerances are ±0.25

Schematics

13F-671B



13F-67C

